FLOATING-POINT DSP

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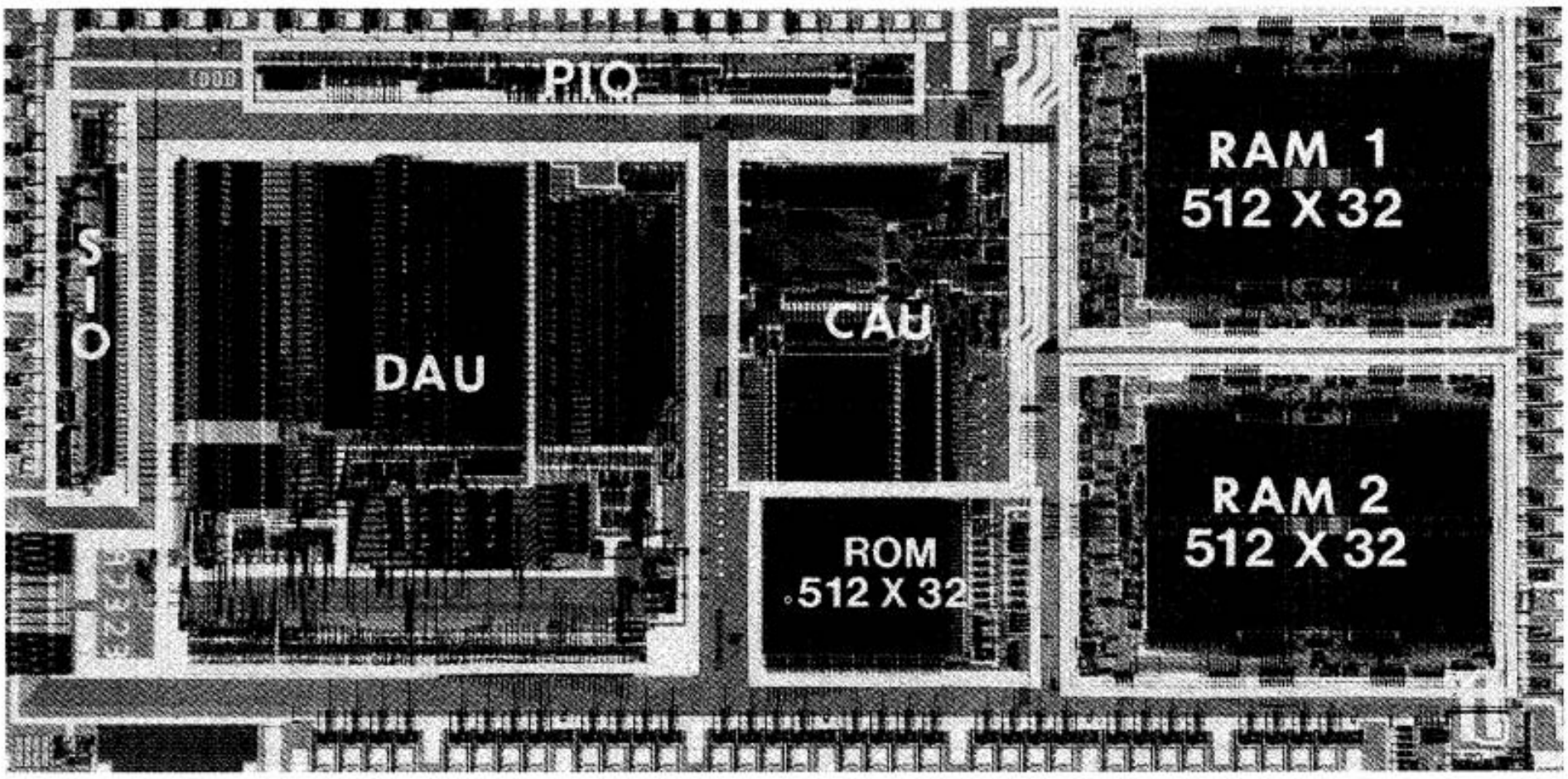

A Programmable Digital Signal Processor with 32B Floating Point Arithmetic

BY 1985, MANUFACTURING technology and design tools had improved to the point that it was possible to create DSP chips that employed floating-point designs. Kershaw et al. described the first floating-point DSP implementation, which freed the programmer from the tedious task of scaling intermediate calculations of an algorithm.

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